

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	718	(702/57,58).CCLS.	US-PGPUB; USPAT	OR	OFF	2005/08/10 10:57
L2	752	(702/81-84).CCLS.	US-PGPUB; USPAT	OR	OFF	2005/08/10 10:59
L3	889	(702/108,117).CCLS.	US-PGPUB; USPAT	OR	OFF	2005/08/10 11:02
L4	693	(702/118-121).CCLS.	US-PGPUB; USPAT	OR	OFF	2005/08/10 11:05
L5	422	(702/123-125).CCLS.	US-PGPUB; USPAT	OR	OFF	2005/08/10 11:05
L6	986	(702/182).CCLS.	US-PGPUB; USPAT	OR	OFF	2005/08/10 11:06
L7	820	(702/183).CCLS.	US-PGPUB; USPAT	OR	OFF	2005/08/10 11:10
L8	1028	(257/203,207,208).CCLS.	US-PGPUB; USPAT	OR	OFF	2005/08/10 11:14
L9	466	(324/500,527).CCLS.	US-PGPUB; USPAT	OR	OFF	2005/08/10 11:14
L10	0	system adj LSI and circuit adj block and @pd>"20050210"	JPO	OR	ON	2005/08/10 11:16
L11	0	system adj large adj scale adj integration and @pd>"20050210"	JPO	OR	ON	2005/08/10 11:16
L12	2938	(324/765).CCLS.	US-PGPUB; USPAT	OR	OFF	2005/08/10 11:17
L13	23	L12 and ((large adj scale adj integration) LSI) and @pd>"20050210"	US-PGPUB; USPAT	OR	ON	2005/08/10 11:17
L14	2540	(438/14,17).CCLS.	US-PGPUB; USPAT	OR	OFF	2005/08/10 11:18
L15	19	L14 and ((large adj scale adj integration) LSI) and @pd>"20050210"	US-PGPUB; USPAT	OR	ON	2005/08/10 11:18
L16	107254	((large adj scale adj integration) LSI)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/08/10 11:19
L17	2	L16 and (circuit adj block) with (test adj voltage) and @pd>"20050210"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/08/10 11:19
L18	0	L16 and (circuit adj block) with second adj voltage and @pd>"20050210"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/08/10 11:20
L19	0	L16 and (circuit adj block) same (simultaneous concurrent) with (test testing tested) and @pd>"20050210"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/08/10 11:20
L20	0	L16 and (circuit adj block) same (simultaneously concurrently) with (test testing tested) and @pd>"20050210"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/08/10 11:20

L21	0	L16 and (circuit adj block) same selectable adj power adj supply and @pd>"20050210"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/08/10 11:21
L22	0	L16 and (test testing tested) same selectable adj power adj supplies and @pd>"20050210"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/08/10 11:21
L23	1	L16 and (test testing tested) with (prior before) adj fabrication and @pd>"20050210"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/08/10 11:21
L24	0	L16 and wafer adj test with circuit adj block and @pd>"20050210"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/08/10 11:22
L25	1	L16 and wafer adj test with (fabricate fabricated fabrication) with (complete completed completing) and @pd>"20050210"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/08/10 11:22
L26	1	L16 and common adj external adj terminal with wire adj bonding	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/08/10 11:23
L27	0	L16 and common adj terminal with wire adj bonding	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/08/10 11:23
L28	1	L16 and power near2 terminal with wire adj bonding and @pd>"20050210"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/08/10 11:23
L29	2	L16 and supply adj terminal same wire adj bonding and @pd>"20050210"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/08/10 11:24
L30	6	L16 and third adj circuit adj block and @pd>"20050210"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/08/10 11:24
L31	107254	((large adj scale adj integration) LSI)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/08/10 11:26

L32	4	L31 and three adj circuit adj blocks and @pd>"20050210"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/08/10 11:26
L33	905	circuit adj block with (switch switching) with (power supply)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/08/10 11:26
L34	15	L31 and L33 and @pd>"20050210"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/08/10 11:26
L35	0	power adj supplies with (switch switching) with circuit adj block and @pd>"20050210"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2005/08/10 11:28

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L36	0	((large adj scale adj integration) LSI) and power and voltage and line and (surround encircle)).clm.	US-PGPUB	OR	OFF	2005/08/10 11:31